

SIBC-961Z Intel® H61 ATX Motherboard (LGA1155 socket for Intel® Second Generation Core™ i7/ Core™ i5/ Core™ i3 Processors) Startup Manual

Packing List

Before you begin installing your card, please make sure that the following items have been shipped:

- 1 x SIBC-961Z ATX Motherboard
- 1 x CD ROM per carton, which contains Drivers
- 1 x SATA cable kit (SATA/Power)
- 1 x I/O Shield
- 1 x Startup Manual per carton

If any of these items are missing or damaged, please contact your distributor or sales representative immediately.

Note 1: Acrobat Reader is required to view any PDF file. Acrobat Reader can be downloaded at: www.adobe.com/Prodindex/acrobat/readstep.html (Acrobat is a trademark of Adobe).

Specifications

System

- CPU:** LGA1155 socket for Intel Sandy Bridge Processor, Intel Core i7-2600 3.4 GHz, Intel Core i5-2400 3.1 GHz, Intel Core i3-2120 3.3 GHz, Pentium G850 2.9 GHz, Celeron G540 2.5 GHz
- FSB:** 1333/1066 MHz
- BIOS:** AMI 32 Mb SPI BIOS
- System Chipset:** Intel H61
- I/O Chipset:** Nuvoton NCT6776F
- Memory:** Two 240-pin DIMM sockets support up to 8 GB Dual Channel DDR3 1333/1066 SDRAM
* Based on OS limits, when using the memory over 4 GB; capacity under 32-bit OS, system will only recognize less than 3 GB. 64-bit OS will not have this limitation.
- Watchdog Timer:** Reset: 1 sec. ~ 255 min. and 1 sec. or 1 min./step
- H/W Status Monitor:** Monitoring temperatures, voltages, and cooling fan status. Auto throttling control when CPU overheat
- Expansion Slots:** 1 x PCI Express x16, 1 x PCI Express x1, 4 x PCI (PCI Rev. 2.3 compliant)
- DIO:** 8-bit
- S3 / S4:** Yes
- TPM:** 2 x10 Pin header
- Wake up on LAN or Ring:** LAN (PME / RPL)
- Smart Fan Control:** Yes

Display

- Chipset:** Integrated HD Graphics 2000
- Max. Resolution:** 2048 x 1536 bpp (@ 75Hz)
- VGA:** Yes, Integrated Graphics
- LVDS / DVI / HDMI:** Through ADD2 LVDS Card
- Secondary VGA:** Yes, through ADD2 card

Audio

- Audio Codec:** Realtek, ALC892, 5.1 HD Audio
- Audio Interface:** Mic in, Line in, Line out

Ethernet

- LAN1:** RTL8111E Gigabit LAN
- LAN2:** RTL8111E Gigabit LAN

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<http://www.shuokesi.com>

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For technical support and service, please visit our support website at:

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This manual is for the SIBC-961Z Series.

Part No. 20060A2110

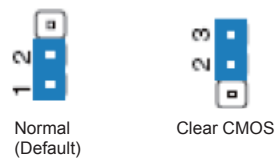

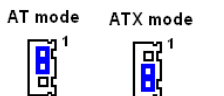
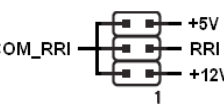
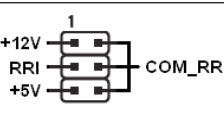
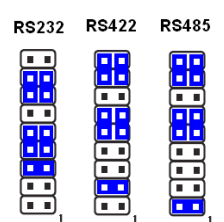
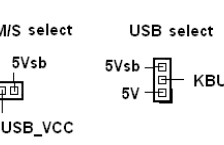
Print in China

1st Edition

July 2018


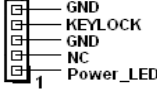
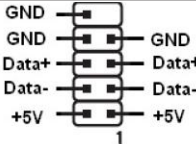
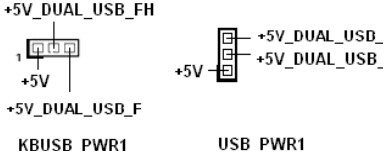
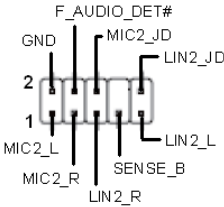
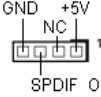
Jumpers and Connectors

The board has a number of jumpers that allow you to configure your system to suit your application. The table below lists the function of each of the jumpers and connectors.

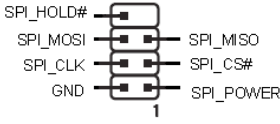

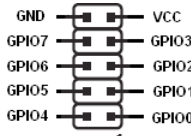
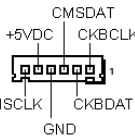
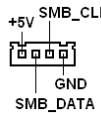
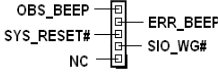
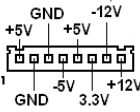
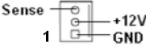
Jumpers		
Label	Function	Note
JCMOS1	Clear CMOS	 <p>Normal (Default) Clear CMOS</p>
SATA_PWR1	SATA_pin7 Voltage select	 <p>GND +5V SATA_pin7</p>
PSCN1	AT/ATX Mode setting	 <p>AT mode ATX mode</p>
JCOMPWR_1	COM1 for Power select 5 V & 12 V	 <p>COM_RRI +5V RRI +12V</p>
JCOMPWR_2	COM2 for Power select 5 V & 12 V	 <p>+12V RRI +5V COM_RRI</p>
JSETCOM2	COM2 RS-232/422/485 Setting jumper	 <p>RS232 RS422 RS485</p>
KBUSB_PWR1 USB_PWR1	K/B,M/S +5 V/+5 Vsb select USB +5 V/+5 Vsb select	 <p>K/B,M/S select USB select</p> <p>5V 5Vsb 5Vsb 5V KBUSB_VCC</p> <p>KBUSB_VCC</p>

Jumpers and Connectors

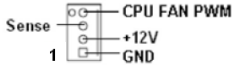
Connectors

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JFP3	KEYLOCK pin Header																															
USB56 USB78 USB910	USB Header																															
KBUSB_PWR1 USB_PWR1	+5 V/+5VSB switch jumper																															
F_AUDIO	Front Panel Audio Header for Intel HD Audio																															
SPDIF_OUT_L	SPDIF header																															

Jumpers and Connectors

SPL_CN1	SPI Headers																									
LANLED1	LAN LED	 <table border="1" data-bbox="546 399 1036 630"> <thead> <tr> <th>Pin</th> <th>Signal</th> <th>Pin</th> <th>Signal</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>LAN1_ACT#</td> <td>2</td> <td>LAN2_ACT#</td> </tr> <tr> <td>3</td> <td>LAN+_VCC3</td> <td>4</td> <td>LAN+_VCC3</td> </tr> <tr> <td>5</td> <td>LAN1_SPEED1G</td> <td>6</td> <td>LAN2_SPEED1G</td> </tr> <tr> <td>7</td> <td>LAN1_SPEED100M</td> <td>8</td> <td>LAN2_SPEED100M</td> </tr> <tr> <td>9</td> <td>LAN+_VCC3</td> <td>10</td> <td>Key (no pin)</td> </tr> </tbody> </table>	Pin	Signal	Pin	Signal	1	LAN1_ACT#	2	LAN2_ACT#	3	LAN+_VCC3	4	LAN+_VCC3	5	LAN1_SPEED1G	6	LAN2_SPEED1G	7	LAN1_SPEED100M	8	LAN2_SPEED100M	9	LAN+_VCC3	10	Key (no pin)
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GPIO1	GPIO																									
KBMS2	Keyboard/ Mouse Header																									
SMBUS1	SM BUS																									
JWDT1+JOBS1	Watch/ HW monitor alarm header																									
VOLT1	Voltage Display header																									
SYSFAN1 SYSFAN2	System FAN Header																									

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Safety Declaration

This device complies with the requirements in Part 15 of the FCC rules. Operation is subject to the following two conditions:

1. This device may not cause harmful interference.
2. This device must accept any interference received, including interference that may cause undesired operation.

Board Diagram

